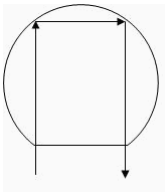
Part Number	Customer

Category		Parameter	Specification	Measurement Method
OverallWafer	1.0	Silicon Supplier	Device Silicon Suplier C - NO CHANGES	IceMOS Confirmed
	2.0	Diameter	150.00 +/- 0.20 mm	
	3.0	Primary Flat Orientation	{110}+/-0.5 degree	Wafer Vendor
	4.0	Primary Flat Length	57.50 +/- 2.50 mm	Wafer Vendor
	5.0	Secondary Flat Orientation	none	
	6.0	Overall Thickness	647.00 +/- 25.00 µm	ADE, 100%
	7.0	Total Thickness Variation (TTV)	<5.00µm	Guaranteed by Process
	8.0	Bow	<100.00µm	ADE to ASTM F534, 20%
	9.0	Warp	<100.00µm	ADE to ASTM F657, 20%
	10.0	Edge Chips	0	Bright Light, 100% (note 2)
	11.0	Edge Exclusion	3mm	
HandleSilicon	12.0	Handle Growth Method	CZ	Wafer Vendor
	13.0	Handle Orientation	{100} +/- 0.5 degree	Wafer Vendor
	14.0	Handle Thickness	525.00 +/- 25.00 µm	ADE, 100%
	15.0	Handle Doping Type	N	Wafer Vendor
	16.0	Handle Dopant	Phosphorous	Wafer Vendor
	17.0	Handle Resistivity	1 - 50 Ohmcm	Wafer Vendor
	18.0	Backside Finish	Polished with oxide and lasermark	Wafer Vendor
BuriedOxide	19.0	Oxide Type	Thermal	
	20.0	Oxide Thickness	20,000.00 +/- 1,000.00 A	Nanospec centre point, 4%
	21.0	Oxide formed on	Handle Wafer	
DeviceSilicon	22.0	Device Growth Method	CZ	Wafer Vendor
	23.0	Device Orientation	{111} +/- 0.5 degree	Wafer Vendor
	24.0	Nominal Thickness	120.00 +/- 1.00 µm	ADE, Single point, 100%
	25.0	Distance to device silicon edge from wafer edge	<= 2.0mm	Typical by process
	26.0	Edge Removal Angle	45 +/- 15 deg	Guaranteed by process
	27.0	Edge Removal Depth in Handle	<100um	Guaranteed by process
	28.0	Device Doping Type	N	Wafer Vendor
	29.0	Device Dopant	Phosphorous	Wafer Vendor
	30.0	Device Resistivity	0.003 ~ 0.01 Ohm-cm	Wafer Vendor
	31.0	Dislocation Etch Pit Density	<100/cm2	Wafer Vendor
	32.0	Voids	0	Bright Light, 100% (note 2)
	33.0	Scratches	0	Bright Light, 100% (note 2)
	34.0	Haze	none	Bright Light, 100% (note 2)

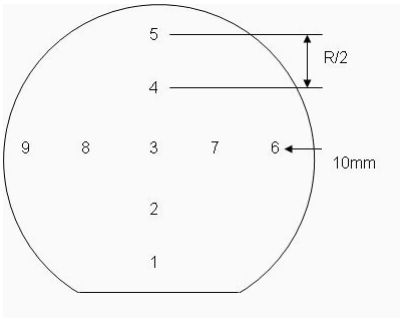
Part Number	Customer

Category	Parameter	Specification	Measurement Method
Shipping Details	Wafer per box :	Max 25	
	Packaging :	Taped Polypropylene Wafer Box Empak, Ultrapak, 150.00mm Antistatic Double Bagging	
	Lot Shipment Data	Device Thickness Bow / Warp Data Handle and SOI Thickness	
Explanatory Notes	1. Microscope inspection performed using microscope scan as below. 5x objective.		



2. All bright light inspections performed exclude all wafer area outside the edge exclusion defined in Overall Wafer, Edge Exclusion. High intensity bright lamp inspection as per ASTM F523.

3. 9 point measurement are as shown in the diagram below:



Additional Information